

APPLICANT(S): IELLICI, Devis et al.  
SERIAL NO.: Not Yet Assigned  
FILED: Herewith  
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## AMENDMENTS TO SPECIFICATION

### In the Specification:

On page 1, line 3, please insert the following:

#### **--CROSS-REFERENCE TO RELATED APPLICATIONS**

This application is a National Phase Application of PCT International Application No PCT/GB2004/002497, International Filing Date June 16, 2004, claiming priority of Great Britain Patent Application 0313890.6, filed June 16, 2003, which are both incorporated by reference herein in their entirety.

#### **FIELD OF THE INVENTION--**

On page 1, line 9 please insert:

#### **--BACKGROUND OF THE INVENTION--**

On page 6, line 21 please insert:

#### **--SUMMARY OF THE INVENTION--**

On page 6, line 27 please insert the following paragraphs:

#### **--BRIEF DESCRIPTION OF THE DRAWINGS**

For a better understanding of the present invention and to show how it may be carried into effect, reference shall now be made by way of example to the accompanying drawings, in which:

FIGURE 1 shows a driven dielectric antenna provided with a parasitic PILA;

FIGURE 2 shows a broadband dielectric antenna mounted in a corner of a PCB with a parasitic PILA passing over a top of the dielectric antenna;

FIGURE 3 shows a dielectric antenna mounted in a corner of a PCB with a parasitic PILA adjacent thereto but not passing over the dielectric antenna;

FIGURE 4 shows a practical hybrid antenna design shaped to fit inside a modern mobile telephone handset casing;

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FIGURE 5 shows an oblong dielectric antenna mounted on a PCB with a parasitic PILA passing thereover;

FIGURES 6(a) and 6(b) show an underside of the PCB of Figure 5 with part of a groundplane removed from a corner portion thereof;

FIGURE 7 shows a dual band WLAN antenna comprising a driven dielectric antenna and a parasitic PILA mounted adjacent thereto; and

FIGURE 8 shows an  $S_{11}$  return loss plot of the antenna of Figure 7.

#### DETAILED DESCRIPTION OF THE INVENTION—

On page 10, line 6 please delete the following paragraphs:

~~For a better understanding of the present invention and to show how it may be carried into effect, reference shall now be made by way of example to the accompanying drawings, in which:~~

~~FIGURE 1 shows a driven dielectric antenna provided with a parasitic PILA;~~

~~FIGURE 2 shows a broadband dielectric antenna mounted in a corner of a PCB with a parasitic PILA passing over a top of the dielectric antenna;~~

~~FIGURE 3 shows a dielectric antenna mounted in a corner of a PCB with a parasitic PILA adjacent thereto but not passing over the dielectric antenna;~~

~~FIGURE 4 shows a practical hybrid antenna design shaped to fit inside a modern mobile telephone handset casing;~~

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~~FIGURE 5 shows an oblong dielectric antenna mounted on a PCB with a parasitic PILA passing thereover;~~

~~FIGURES 6(a) and 6(b) show an underside of the PCB of Figure 5 with part of a groundplane removed from a corner portion thereof;~~

~~FIGURE 7 shows a dual band WLAN antenna comprising a driven dielectric antenna and a parasitic PILA mounted adjacent thereto; and~~

~~FIGURE 8 shows an  $S_{11}$  return loss plot of the antenna of Figure 7.~~